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| U.S. Department of Commerce, Patent and Trademark Office | | Docket No. | | Serial No. | | | |
| (PTO Form 1449 modified) | | DIVA/993 | | 10/665,738 | | | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | Applicant: | | Confirmation No.: | | | |
| LIST OF PATENTS AND PUBLICATIONS CITED BY APPLICANT | | Cleary, Brown | | 4692 | | | |
| (Use several sheets if necessary) | | Filing Date | | Group | | | |
| Examiner Unknown | | 9/19/03 | | 2143 2109 | | | |
| U.S. Patent Documents | | | | | | | |
| *Examiner Initial | | Document Number | Issue Date | Applicant(s) Name | Class | Subclass | Filing Date If Appropriate |
| | A1 | | | | | | |
| | A2 | | | | | | |
| | A3 | | | | | | |
| | A4 | | | | | | |
| | A5 | | | | | | |
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| | A10 | | | | | | |
| | A11 | | | | | | |
| Foreign Patent Documents | | | | | | | |
| *Examiner Initial | | Document Number | Date | Country | Class | Subclass | Translation |
| | | | | | | | YES NO |
| | B1 | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
| | B2 | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
| | B3 | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
| | B4 | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
| | B5 | | | | | | <input type="checkbox"/> <input type="checkbox"/> |
| OTHER ART | | | | | | | |
| *Examiner Initial | | Including Author, Title, Date, Pertinent Pages, Etc. | | | | | |
| /MW/ | ✓C1 | "MultiProcessor Specification," version 1.4, published by Intel, May 1997 | | | | | |
| /MW/ | ✓C2 | Network Time Protocol (Version 3) Specification, Implementation, RFC 1305 | | | | | |
| | C3 | | | | | | |
| | C4 | | | | | | |
| | C5 | | | | | | |
| Examiner /Michael Wilser/ | | Date Considered 04/24/2007 | | | | | |
| *EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant. | | | | | | | |